

What Is Claimed Is:

1. An information storage device comprising:

one or more semiconductor memories;

an information processing part that, according to an operation program, reads data stored in the one or more semiconductor memories, writes data to one or more semiconductor memories, and performs other operations; and

a transfer processing part that, if read data read from the semiconductor memories contains minor errors the counts of which is equal to or less than a predetermined counts, corrects the minor errors and transfers the data, and if the read data contains major errors the counts of which is more than the predetermined counts, transfers the data without correcting the major errors.

2. The information storage device according to claim 1,

wherein the transfer processing part comprises:

a management data creation part that creates management data used for data corrections from write data transferred from the outside;

an error detection part that detects whether the read data contains error locations, based on the read data and the management data created by the management data creation

part; and

a first data correction and calculation part that, when errors are detected by the error detection part, calculates the correction locations and the correction data of the read data, and judges whether the read data contains major errors or minor errors, and

wherein the information processing part, when the first data correction and calculation part judges that the read data contains minor errors, replaces the read data by the correction data in the correction locations thereof calculated by the first data correction and calculation part, and transfers the corrected data to the outside, and when the first data correction and calculation part judges that the read data contains major errors, transfers the read data to the outside without correcting the read data.

3. The information storage device according to claim 2,

wherein the first data correction and calculation part, when the read data contains N or fewer errors, judges the errors as minor errors, and if the read data contains N plus one or more errors, judges the errors as major errors.

4. An information storage device comprising:

one or more semiconductor memories;

an information processing part that, according to an

operation program, reads data stored in the one or more semiconductor memories, writes data to the one or more semiconductor memories, and performs other operations; and

a transfer processing part that, if read data read from the semiconductor memories contains one error, corrects the error and transfers the read data, and if the read data contains two or more errors, transfers the read data to the outside without correcting the errors.

5. The information storage device according to claim 4,

wherein the transfer processing part comprises:

a management data creation part that creates management data used for data corrections from write data transferred from the outside;

an error detection part that detects whether the read data contains error locations, based on the read data and the management data created by the management data creation part; and

a first data correction and calculation part that, when an error is detected by the error detection part, calculates the correction locations and the correction data of the read data, and judges whether the read data contains two or more errors, or one error, and

wherein the information processing part, when the

first data correction and calculation part judges that the read data contains one error, replaces the read data by the correction data in the correction location thereof calculated by the first data correction and calculation part, and transfers the corrected data to the outside, and when the first data correction and calculation part judges that the read data contains two or more errors, transfers the read data to the outside without correcting the read data.

6. An information processing system comprising: an information storage device including one or more semiconductor memories, and an information processing part that, according to an operation program, reads data stored in the one or more semiconductor memories, writes data to the one or more semiconductor memories, and performs other operations; and an information processing device managing the information storage device,

wherein the information storage device includes a transfer processing part that, if read data read from the semiconductor memories contains minor errors the counts of which is equal to or less than a predetermined counts, corrects the minor errors and transfers the read data, and if the read data contains major errors the counts of which is more than the predetermined counts, transfers the read

data without correcting the major errors, and

wherein the information processing device includes a second data correction and calculation part that corrects the read data containing the major errors transferred from the information storage device.

7. The information processing system according to claim 6,

wherein the transfer processing part comprises:

a management data creation part that creates management data used for data corrections from write data transferred from the outside;

an error detection part that detects whether the read data contains error locations, based on the read data and the management data created by the management data creation part; and

a first data correction and calculation part that, when errors are detected by the error detection part, calculates the correction locations and the correction data of the read data, and judges whether the read data contains major errors or minor errors, and

wherein the information processing part, when the first data correction and calculation part judges that the read data contains minor errors, replaces the read data by the correction data in the correction locations thereof

calculated by the first data correction and calculation part, and transfers the corrected data to the outside, and when the first data correction and calculation part judges that the read data contains major errors, transfers the read data to the outside without correcting the read data.

8. The information processing system according to claim 7,

wherein the first data correction and calculation part, when the read data contains N or fewer errors, judges the errors as minor errors, and if the read data contains N plus one or more errors, judges the errors as major errors.

9. An information processing system comprising: an information storage device including one or more semiconductor memories, and an information processing part that, according to an operation program, reads data stored in the one or more semiconductor memories, writes data to the one or more semiconductor memories, and performs other operations; and an information processing device managing the information storage device,

wherein the information storage device includes a transfer processing part that, if read data read from the semiconductor memories contains one error, corrects the error and transfers the read data, and if the read data contains two or more errors, transfers the read data to the

outside without correcting the errors, and

wherein the information processing device includes a second data correction and calculation part that corrects two or more errors in the read data transferred from the information storage device.

10. The information processing system according to claim 9,

wherein the transfer processing part comprises:

a management data creation part that creates management data used for data corrections from write data transferred from the outside;

an error detection part that detects whether the read data contains error locations, based on the read data and the management data created by the management data creation part; and

a first data correction and calculation part that, when an error is detected by the error detection part, calculates the correction locations and the correction data of the read data, and judges whether the read data contains two or more errors, or one error, and

wherein the information processing part, when the first data correction and calculation part judges that the read data contains one error, replaces the read data by the correction data in the correction location thereof

calculated by the first data correction and calculation part, and transfers the corrected data to the outside, and when the first data correction and calculation part judges that the read data contains two or more errors, transfers the read data to the outside without correcting the read data.